

Figure 1

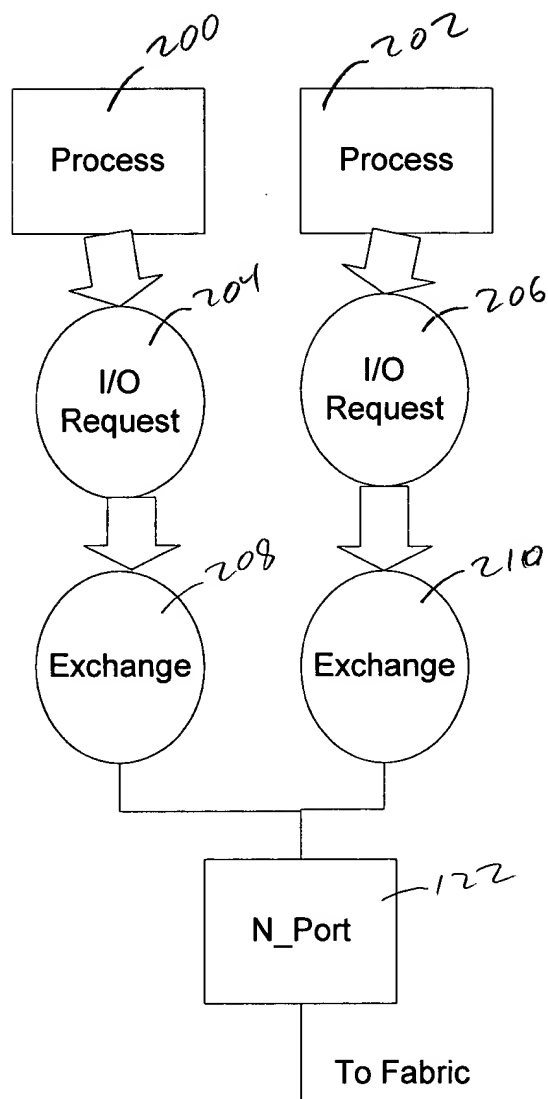


Figure 1A

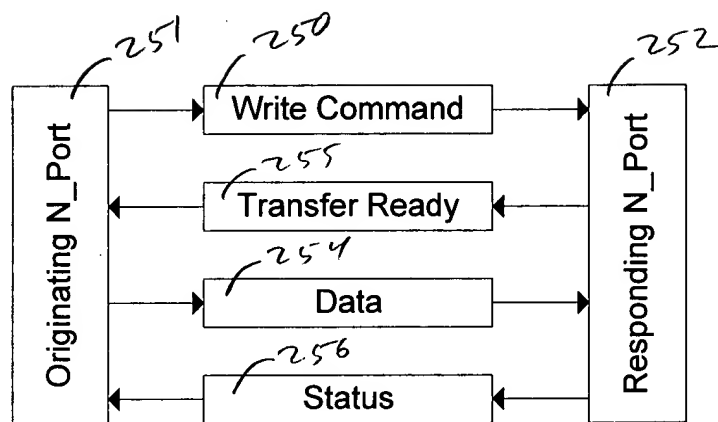


Figure 2A

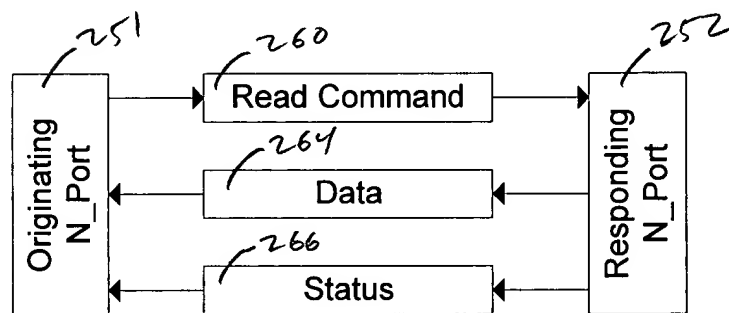
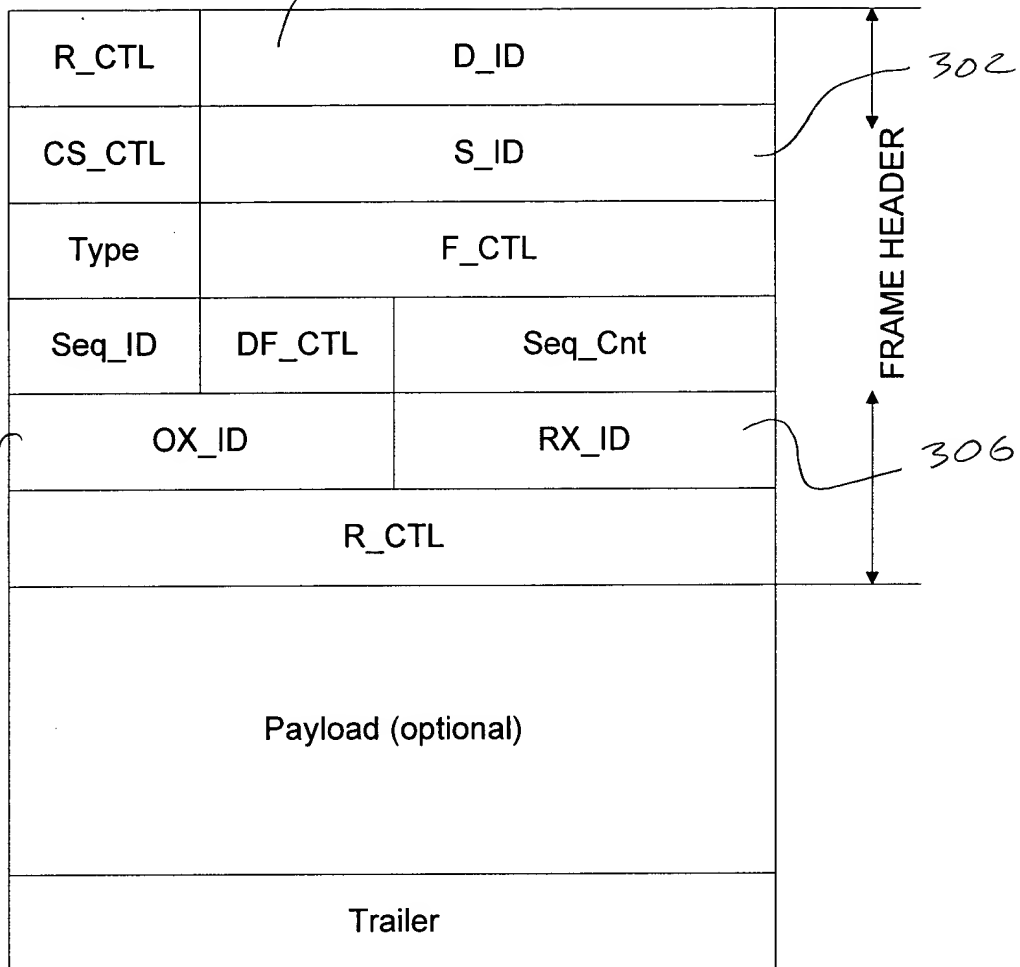


Figure 2B

Simple Exchange Sequences



Packet Structure

Figure 3

Figure 2A is a block diagram illustrating a prior art system. It shows a sequence of operations: an input labeled 300 D_ID enters a block labeled Hash Function. The output of the Hash Function is an Address, labeled 334 . This Address is then used as input to a block labeled Routing Table, which is labeled 330 . The output of the Routing Table is an Outgoing Port Selector, labeled 336 . The entire diagram is labeled "Prior Art" at the bottom.

Figure 3A

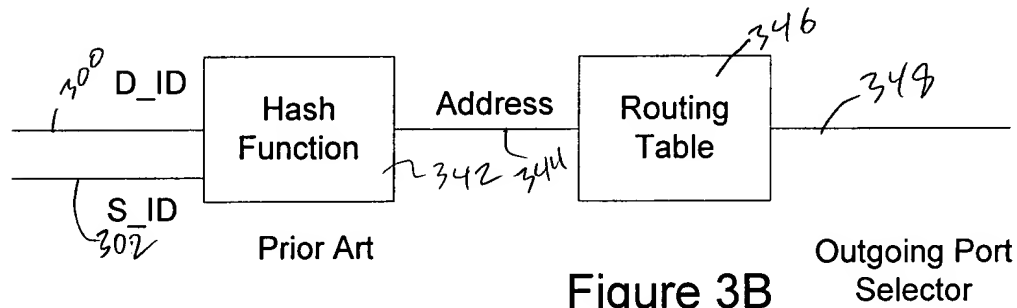


Figure 3B

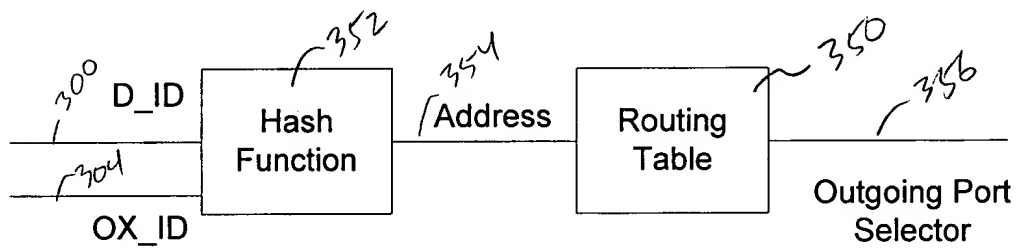


Figure 3C

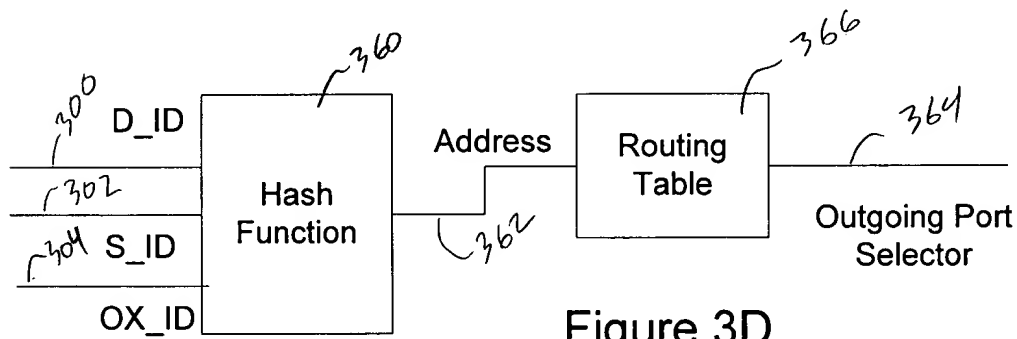


Figure 3D

